

#### ABSTRACT OF THE DISCLOSURE

An electrolytic capacitor with a further lowered impedance is provided. In the electrolytic capacitor in accordance with the present invention, respective currents  
5 directed opposite from each other flow through anode and cathode vias extending along the thickness of a substrate. The anode vias are lopsidedly located in a marginal area of an anode electrode pattern, and thus are disposed significantly close to the cathode vias formed in a cathode  
10 electrode pattern disposed close to the anode electrode pattern. The cathode vias are lopsidedly located in a marginal area of the cathode electrode pattern, and thus are disposed significantly close to the anode vias. Since the anode and cathode vias are formed close to each other as  
15 such, this electrolytic capacitor achieves a lower ESL, thereby attaining an impedance lower than that of a conventional electrolytic capacitor.